



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

September 20, 2006 ABeggs

Date

Alexandra Beggs

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant	: Ronnie M. Harrison	Attorney Docket No.:	500395.02
Patent No.	: US 6,954,097 B2	Serial No.	: 09/758,970
Issue Date	: October 11, 2005	Filed	: January 9, 2001
Title	: METHOD AND APPARATUS FOR GENERATING A SEQUENCE OF CLOCK SIGNALS		

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate  
SEP 28 2006  
of Correction**

Sir:

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Item (56),	"5,444,567 A 8/1995	--5,444,667 8/1995
References Cited,	Kataoka.....365/233"	Obara.....365/233--
U.S. Patent		

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## Documents

Item (56), References Cited, Other Publications, Park Reference	“Fast Acquisition Frequency Synthesizer with the Multiple Phase Detectors”,	“Fast Acquisition Frequency Synthesizer with Multiple Phase Detectors,”--
Column 4, Line 34	“and generating the second”	--and generates the second--
Column 4, Lines 46- 47	“embodiment of clock generator in accordance with present invention”	--embodiment of the clock generator in accordance with the present invention--
Column 4, Line 52	“with present invention.”	--with the present invention.--
Column 4, Line 62	“in accordance with”	--in accordance with the--
Column 6, Line 16	“illustrated in FIG. 3. there”	--illustrated in FIG. 3 there--
Column 7, Line 2	“to a phase detector”	--a phase detector--
Column 7, Line 19	“delay of second signal”	--delay of the second signal--
Column 8, Line 67	“or a Peripheral component”	--or a peripheral component--
Column 9, Line 1	“operator or an electronic”	--operator of an electronic--
Column 9, Line 12	“disk drives floppy disk”	--disk drives, floppy disk--
Column 11, Line 39	“first clock signals”	--first clock signal--

The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent or in the original application. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication to Deposit Account No. 50-1266. A duplicate copy of this sheet is enclosed.

Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date: September 19, 2006

By: Edward W. Bulchis

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Enclosures:

Postcard

Form PTO-1050 (+ copy)

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INVENTOR(S) : Ronnie M. Harrison

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MAILING ADDRESS OF SENDER:

**DORSEY & WHITNEY LLP**  
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FORM PTO-1050 (REV. 3-82)

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